# FACULTY PROFILE

## P. Anil Kumar

Department:Electronics and Communication Engineering

College: SR Gudlavalleru Engineering College

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## Address for Communication

P. Anil Kumar

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Vishwabrahamana Colony

Machilipatnam

Krishna District-521001

## Academic Qualifications:

| S.No. | Name of the Degree | University | Percentage of Marks/Grade | Specialization | Year of Pass |
|-------|--------------------|-----------|---------------------------|----------------|--------------|
| 1 | Ph. D | Pondicherry University | - | Biomedical Signal Processing | **Pursuing** |
| 2 | M.Tech | J.N.T.U.K Kakinada | 86.3 | Digital Electronics and Communication Systems | 2014 |
| 3 | B. Tech | J.N.T.U.K Kakinada | 73.5 | Electronics and Communication Engineering | 2009 |
| 4 | Diploma | S.B.T.E.T. Hyderabad | 87.2 | Electronics and Communication Engineering | 2008 |
| 5 | S.S.C | B.S.E Hyderabad | 80.2 | --- | 2003 |

## Professional Experience:

| S.No. | Designation | Institution Name | Working Period | |
|---|---|---|---|---|
| | | | From | To |
| 1 | Assistant Professor | Gudlavalleru Engineering College, Gudlavalleru | 20-05-2013 | Till date |
| 2 | Assistant Professor | Gudlavalleru Engineering College, Gudlavalleru | 15-06-2009 | 31-10-2011 |

## Professional Body Membership:

| S. No | Name of the Professional body | Membership Number |
|---|---|---|
| 1 | Institution of Engineers India | AM 162844-8 |

# Virtual Labs Designed: 1

1. P. Anil Kumar Received an appreciation certificate from Indian Institute of Technology Roorkee Department of Electrical Engineering for the development of the web-enabled experiments (Virtual LAB) of Analog Electronics Lab under the Electronics & Communication Engineering discipline.

# Papers Published in Journals No.:15

1. Penta Anil Kumar, Ramalingam Gunasundari, "A lightweight adaptive spatial channel attention efficient net B3 based generative adversarial network approach for MR image reconstruction from under sampled data," Magnetic Resonance Imaging, Volume 117, Published online December 11, 2024. doi:10.1016/j.mri.2024.110281.

2. P. Anil kumar, M.Puja, P. Rajani, P. Pravallika, M. Sai Suma, "Skin Cancer Detection Using Efficientnet Architecture," Journal Of Engineering, Computing & Architecture, Volume 14, Issue 04, pp. 88-103, April – 2024, ISSN NO:1934-7197, http://www.journaleca.com/JECA/3437.

3. P. Anil Kumar, Thanuri Bhanu Harshitha, Yarlagadda Rajeswari, Sanaka Venkata, Karthikeya, Ramireddy Usha Kumari, "Classification Of Bone Tumor using Efficient-Net B0," International Research Journal of Engineering and Technology (IRJET), Volume: 11 Issue: 04, pp. 732-736, April 2024, e-ISSN: 2395-0056, p-ISSN: 2395-0072.

4.  "Lung Tumor Classification and Detection using Deep CNN ", International Journal of Novel Research and Development (www.ijnrd.org), ISSN:2456-4184, Vol.8, Issue 4, page no.e71-e77, April-2023, Available :http://www.ijnrd.org/papers/IJNRD2304412.pdf

5.  "Brain Tumor Detection using Mobilenet", International Journal of Novel Research and Development (www.ijnrd.org), ISSN:2456-4184, Vol.8, Issue 4, page no.d443-d448, April-2023, Available :http://www.ijnrd.org/papers/IJNRD2304363.pdf

6.  Kumar, P.A., Gunasundari, R. & Aarthi, R. RE-SHFC: Renyi Entropy-Based Spotted Hyena Fractional Calculus Algorithm for MR Image Reconstruction. *Sens Imaging* **23**, 8 (2022). https://doi.org/10.1007/s11220-022-00377-3

7.  Penta Anil Kumar, R Gunasundari, R Aarthi, Fractional Sailfish Optimizer with Deep Convolution Neural Network for Compressive Sensing Based Magnetic Resonance Image Reconstruction, *The Computer Journal*, Volume 66, Issue 2, February 2023, Pages 280–294, https://doi.org/10.1093/comjnl/bxab160.

8.  Penta Anil Kumar, Gunasundari Ramalingam, Aarthi Ramalingam, "Systematic Analysis and Review of Magnetic Resonance Imaging (MRI) Reconstruction Techniques", Current Medical Imaging 2021; 17(8). https://doi.org/10.2174/1573405616666210105125542.

9.  P. Anil Kumar, Sunkara Rajeev, Veera Punnaiah Manda, " MRI Image Reconstruction using Compressive Sensing", International Journal of Recent Technology and Engineering (IJRTE), ISSN: 2277-3878, Volume-8 Issue-2, July 2019, PP:5256-5260.

10. P. Anil Kumar, L. Hema Harshitha, K. Hemanth, G. Chandrika, L. Sai Spandana, *"Multi Face Detection and Recognition"*, Journal of Applied Science and Computations, Volume VI, Issue IV, April/2019, ISSN NO: 1076-5131, PP: 1594-1601.

11. P. Anil Kumar, P. Bhargavi, "LBP Based Sharpness Parameter Based Approach for Defocus Blur Acts on Robust Segmentation algorithm", International Journal of Professional Engineering Studies (IJPRES), Vol.8, Issue 4, May 2017.

12. P.Anil Kumar, P. Divya and M. Kamaraju, "Satellite Remote Sensing Image Based Aircraft Recognition Using Wavelet and Curvelet Transforms", International Advanced Research Journal in Science, Engineering and Technology(IARJSET), Vol. 3, Issue 7, July 2016, ISSN (Online) 2393-8021, ISSN (Print) 2394-1588.

13. P. Anil Kumar, A. Venkata Suresh and M. Kamaraju, "Reconfigurable Low Power FPGA Design with Low Density", International Journal of Innovative Research in Advanced Technology, Volume-2, Issue-12, ISSN: 2319-1467.

14. P. Anil Kumar, Akula Venkata Subba Rao, "Hardware Implementation of Modified Weighted Median Filtering on FPGA", International Journal of Computer Applications, Volume 105 – No. 4, November 2014, ISSN: 0975 – 8887.

15. Certification for presented the paper entitled "Interactive Multimedia e- Learning System by Logistics Engineering" in the ISTE Fourth Annual Convention during 21-22 March 2009 at Gudlavalleru Engineering College, Gudlavalleru.

## Papers Published in Conferences No.: 5

1. Penta Anil Kumar and R. Gunasundari, "MR Image Reconstruction from Undersampled Data using Multi Dense Residual Block Network with Spatial and Channel Attention Mechanism and Entropy Loss Function," 2023 Second International Conference on Advances in Computational Intelligence and Communication (ICACIC), Puducherry, India, 2023, pp. 1-7, doi: 10.1109/ICACIC59454.2023.10434987.

2. P. Anil Kumar, M.Kamaraju, "Wireless Fingerprint Attendance Management System", 2015 IEEE International Conference on Electrical, Computer and Communication Technologies, 05-07 March 2015 at SVS College of Engineering, Coimbatore, Tamil Nadu, India. PP.1342-1347.

3. P. Anil Kumar, M.Kamaraju, "DSP Based Embedded Fingerprint Recognition System", IEEE Conference on Hybrid Intelligent Systems (HIS - 2013) during 04th -06th December 2013, ISBN: 978-1-4799-2439-4/13 at Yassmine Hammamet, Tunisia,pp.6-11.

4. P.Anil Kumar, M. Kamaraju, B. Ananda Krishna, B. Rajasekhar, "Embedded Fingerprint Recognition System", Recent Researches in Telecommunications, Informatics, Electronics and Signal Processing, ISBN: 978-960-474-330-8,Sept 17-19,2013 at Baltimore,MD, USA,pp.394-401.

5. P.Anil Kumar, M.Kamaraju,B. Ananda Krishna, B. Rajasekhar,"A Novel Implementation of Fingerprint Recognition System using DSP Processor", AAMT-2013,AAMT-2013, pp. 3-6, ISSN:2278-0181, March-2013.

## Workshops Organized No.: Nil

## Guest Lectures Organized:1

1. Organized a Guest Lecture on "Entrepreneurship Development" for III Year students on 15th December 2018.

## Workshops AttendedNo.:12

1. Participated in Two Day Online Workshop on "5G NOMA Communications" organized by Department of Electronics and Communication Engineering, CVR College of Engineering, Ibrahimpatnam, Hyderabad during 12th -13th August, 2022.

2. Participated in Two Day Workshop on "Machine Learning: Concepts and Applications" organized by R&D Cell, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru during 30th June-1st July, 2022.

3. Participated in One Week Online Workshop on "Blended E- Learning" organized by Department of Computer Science and Engineering, JNTUK University College of Engineering Vizianagaram in association with Computer Society of India (Region-V) from 6th to 10th July 2020.

4. Workshop on VLSI Circuits Analog & Digital Design Perspectives conducted by Department of Electronics and Communication Engineering, JNTA College of Engineering Anantapuramu (Autonomous) in association with AMS Semiconductors & HCL Technologies from 16-05-2020 to 17-05-2020.

5. One Day Workshop on "Research in Energy Management Wireless Networks and Smart Transportation Systems" organized by the Institute of Engineers (IE), INDIA, Vijayawada local Center, Vijayawada on 29th March 2015.

6. One Day Workshop on "Ultra Low Power Communication Using CC430 Microcontroller – Hands on Experience", organized by the Department of Electronics and communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 09th February 2015.

7. Two-Week ISTE Workshop on "Control Systems", conducted by Indian Institute of Technology Kharagpur from 2nd to 12th December 2014 at Gudlavalleru Engineering College, Gudlavalleru.

8. One Day Workshop on "Speech Processing: Current Challenges and Hands-on Experience", organized by Signal Processing and Communication Research Group, Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 23rd August 2014.

9. Two-Week ISTE Workshop on "Signals & Systems", conducted by Indian Institute of Technology Kharagpur from 2nd to 12th January 2014 at Gudlavalleru Engineering College, Gudlavalleru.

10. A Two-Day Workshop on "Issues in Integrated Circuits (ICs) Design" held during 13th to 14thDecember 2013 organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru.

11. One-Week Workshop on "Training Teachers for Excellence" organized by Gudlavalleru Engineering College from 17th to 21th June 2013, conducted by Dr B.G. Barki, Former Director, NITTTR, Chennai.

12. A One Day Workshop on "Analog, Digital and Mixed Signal VLSI Design" on 15th October 2011 organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru.

## Webinars Attended No.: 19

1. Attended One day Webinar titled "Research Metrics and Software Tools" organized by Research and Development Cell, LBRCE on 20th September 2021.

2. Attended a Webinar on the topic "Yours Research fully-Covid-19 Period" conducted by Department of Electrical & Electronics Engineering, Swarnandhara College of Engineering & Technology, Narsapur on 27-06-2020.

3. Participated in a Webinar on the topic "Internet of Things and it's Applications" conducted by Department of Electrical & Electronics Engineering, Kakinada Institute of Technology and Science, Tirupathi(V), Divili on 27-06-2020.

4. Participated in a Webinar on the topic "Impact of COVID in Engineering Education & Way forward" organized by Indian Society for Technical Education (ISTE) Chapter, Gudlavalleru Engineering College, Gudlavalleru on 9th June 2020.

5. A Live Webinar on "Smart Manufacturing and Industry 4.0." held on 06th June 2020 Organized by the Department of Electronics and Communication Engineering, Mekapati Rajamohan Reddy Institute of Technology & Science.

6. A webinar on "Brain Computer Interface" on 5th June 2020 Hosted by NITK-STEP in association with PANTECH.

7. National Level Webinar on "Physical Activity, Immunity and Covid-19" organized by A. V. Patel Commerce College on June 5th, 2020.

8. Webinar on "Intellectual Property Rights and their Importance" organized by K L University, Andhra Pradesh dated 02 June 2020.

9. A webinar on "Brain Controlled Robot Design" on 6th June 2020 Hosted by NITK-STEP in association with PANTECH.

10. Virtual Summit on "COVID-19: Way on Education, Technology, Environment & Mankind" Conducted on 30th May 2020 organized by Andhra Loyola Institute of Engineering and Technology and Communication Engineering.

11. Webinar on "Writing an Effective Research Paper-Means and Methods" organized by Department of Computer Science & Engineering, CMR Engineering College on 29th May 2020.

12. A Webinar on "Radio Frequency Measurements for Cellular and Wireless Communication Systems" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College on 25th May 2020.

13. A Technical Webinar on "Design Approaches for Frontend VLSI Design" organized by Department of Electronics and Communication Engineering, Vaagdevi College of Engineering (Autonomous).

14. "Webinar on Advanced IoT Applications - A Practical Approach", an online training program offered remotely on 23rd May 2020, organized by Department of Computer Science & Engineering, Lakireddy Bali Reddy College of Engineering (Autonomous).

15. A Webinar on "Future of AI" on 20th May 2020 Hosted by IETE Mumbai in association with PANTECH.

16. IT Live webinar on "How to Prepare a Good Research Grant Proposal?" on May 20, 2020, organized by Department of Information Technology, PSNA College of Engineering and Technology.

17. Participated in a Webinar on the topic "Speech Signal Processing" organized by Lakireddy Bali Reddy College of Engineering on 17-5-2020.

18. Participated in a Webinar on the topic "Node.js" organized by Lakireddy Bali Reddy College of Engineering on 17-5-2020.

19. Participated in a Webinar on the topic "Microservices Architecture" organized by Lakireddy Bali Reddy College of Engineering on 16-5-2020.

**Faculty Development/Faculty Training Programs/Seminars AttendedNo.:45**

1. Participated in a Two-Week Faculty Development Program (Online Mode) on "Transforming Education: Integrating OBE and NEP 2020" from 22 July to 2 August 2024, Organized by the Department of Electronics and Communication Engineering, CVR College of Engineering.

2. Participated in a Two-Week Faculty Development Program (Online Mode) on "Transforming Education: Integrating OBE and NEP 2020" from 22 July to 2 August 2024, Organized by the Department of Electronics and Communication Engineering, CVR College of Engineering.

3. Participated in the three-day Online Faculty Development Program on "Generative AI" organized by the Department of Information Technology of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 26 June 2024 to 28 June 2024.

4. Participated in A Two day Faculty Development Program on "Design of Integrated Circuits using Microwind Tools" organized by the Department of Electronics and Communication Engineering of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 30-08-2023 to 31-08-2023.

5. Participated in A one week National Level Faculty Development Program on "Applications of Computer Vision using Deep Learning" organized by the Department of Electronics and Communication Engineering of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 20-02-2023 to 25-02-2023.

6. Participated in A two day Faculty Training Program on "Full Custom IC Design using CADENCE software tools" organized by the Department of Electronics and Communication Engineering of Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru from 04-08-2022 to 05-08-2022.

7. Participated in the Faculty Development Programme on "Insights of NEP 2020 and Strategies for its Implementation" organized by the Academic Strengthening and Advancement Cell, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, on 12th June 2022.

8. Participated in the AICTE-ISTE sponsored one week online induction programme on "Millimeter wave frequencies" organized by the Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, from 17th to 24th August 2021.

9. Participated in the Five Day Online FDP on "Emerging Technologies in Electronics Engineering" organized by the Department of Electronics and Communication Engineering, Godavari Institute Of Engineering & Technology(A), Rajamahendravaram from 29-06-2021 to 02-07-2021.

10. Participated in One Week National Level Online Faculty Development Program on "Trends in Embedded Systems and IoT" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College during 20-25 June 2020.

11. Participated & completed successfully AICTE Training and Learning (ATAL) Academy Online FDP on "Data Sciences" from 2021-1-19 to 2021-1-23 at Gudlavalleru Engineering College.

12. Participated & completed successfully AICTE Training and Learning (ATAL) Academy Online FDP on "Internet of Things" from 2020-12-7 to 2020-12-11 at Gudlavalleru Engineering College.

13. Participated in AICTE Sponsored Online ShortTerm Training Programme on "Blockchain Architecture Design and Use Cases" Phase -III organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 24-08-2020 to 29-08-2020.

14. Participated in AICTE Sponsored Online Short Term Training Programme on "Blockchain Architecture Design and Use Cases" Phase -II organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 17-08-2020 to 22-08-2020.

15. Participated in AICTE Sponsored Online ShortTerm Training Programme on "Blockchain Architecture Design and Use Cases" Phase -I organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College from 1-08-2020 to 15-08-2020.

16. Participated in a National Level Online Faculty Development Program on "Cyber Security" organized by Departments of Computer Science and Information Technology in association with CyberPsy, Gudlavalleru Engineering College, Gudlavalleru from 22nd - 26th July 2020.

17. Participated in a Faculty Development Program on "Insight Into Data Science" organized by Department of Information Technology, CMR Engineering College, Hyderabad from 23-07-2020 to 24-07-2020.

18. Participated in a One Week Faculty Development Program on "Recent Advances in Biomedical Applications and Communication Networks" organized by Department of Electronics and Communication Engineering, GMR Institute of Technology, Rajam during 13th -18th July 2020.

19. Participated in One Week Online Faculty Development Program on "IoT & Machine Learning" organized by Department of Electronics and Communication Engineering, GPREC, Kurnool from 26th June to 30th June 2020.

20. Participated in a Three Day International Online Faculty Development Programme on "Technologies for Healthcare" organized by Department of Electronics and Communication Engineering, GMR Institute of Technology, Rajam, Ap during 8th June to 10th June 2020.

21. Participated in One Week International Online Knowledge Development Program on "Challenges and Advancements in the Design of IoT, Embedded and VLSI Systems: A Researchers View" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, from 8th -13thJune 2020.

22. Faculty Development Program on the Topic "Multi Carrier Modulation in Broadband Communication and Technology Challenges in 5G" organized by Department of Electronics and Communication Engineering, Raghu Engineering College (Autonomous), Vishakhapatnam from 29th May to 30th May 2020.

23. Faculty Development Program on "Artificial Intelligence and its Applications" Conducted by Department of Computer Science and Engineering, Ramachandra College of Engineering in collaboration with 360DigiTMG from 25th May to 30th May 2020.

24. Online Faculty Awareness Program on "Research Methodology" organized by Department of Mechanical Engineering, Rajgad Dnyanpeeth's Shri Chhatrapati Shivajiraje College of Engineering, Pune-412206 during the period of 20/05/2020 to 25/05/2020.

25. Five Day National Level Online FDP on "Modern Research Trends in Communication, Signal Processing and VLSI/MEMS", organized by the Department of Electronics, Andhra Loyola Institute of Engineering and Technology and Communication Engineering, from 19th to 23rd May 2020.

26. Two Day Online FDP on "Microstrip Antennas for Advanced Wireless Communication" organized by Department of Electronics and Communication Engineering, Kallam Haranadhareddy Institute of Technology, in association with IETE, Vijayawada Division from 15th May to 16th May.

27. One Week Online Faculty Development Program on "Advanced Antenna Design Using HFSS" Organized by Department of ECE, Santhiram Engineering College, Nandyal in collaboration with Vardaman College of Engineering, Hyderabad from 12-05-2020 to 17-05-2020

28. One Week Online Faculty Development Program on "Research Challenges and Opportunities Post COVID-19 (RECOP 2020)" organized by Research & Development Cell and Institution's Innovation Council of Sri Vasavi Engineering College (Autonomous) during 4-9 May 2020.

29. One Week Faculty Development Program (FDP) on "Bio- Inspired Algorithms for Intelligent Signal Processing" organizes by E& ICT Academy, NIT Warangal at Vardhaman College of Engineering (Autonomous), Shamshabad, Hyderabad from 24th - 30thApril 2019.

30. One Week Faculty Development Program (FDP) on "Machine Learning Applications in Signal Processing" organizes by E& ICT Academy, NIT Warangal at Gudlavalleru Engineering College (Autonomous), Gudlavalleru from 04th -09thApril 2019.

31. Attended the Two Day Training Programme on "Design and Development of Digital Circuits using VLSI Tools" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru during 6th December to 7th December 2018.

32. Attended the one-week faculty development program on "Communication and Signal Processing" organized by Department of Electronics and Communication Engineering,

Gudlavalleru Engineering College, Gudlavalleru during 29<sup>th</sup> October to 3<sup>rd</sup> November 2018.

33. Completed the NPTEL-AICTE Faculty Development Programme (FDP) funded by the Ministry of HRD, Govt. of India in the course "Switching Circuits and Logic Design" during July to October 2018.

34. Attended a one-week National Level Faculty Development Program (FDP) on "Signal and Image Processing LabVIEW" held during 13<sup>th</sup> -18<sup>th</sup> November 2017 at Gudlavalleru Engineering College, organized by signal processing research group of Department of Electronics and Communication engineering in Association with NI Systems Pvt. Ltd.

35. Attended the Two-Day Faculty Training Program on "Advances in VLSI Design and Embedded System" organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, during 6<sup>th</sup> - 7<sup>th</sup> November 2017.

36. Participated in a Six Day Continuing Education Programme under TEQIP-II on "Internet of Things (IoT)" organized by the department of Electronics and Communication Engineering, NIT Warangal, during February 27<sup>th</sup> -March 04<sup>th</sup>, 2017.

37. Participated in a Two-Week ISTE STTP on CMOS, Mixed Signal and Radio frequency VLSI Design conducted by Indian Institute of Technology Kharagpur from 30<sup>th</sup> January to 4<sup>th</sup> February 2017. This workshop was held under the National Mission on education through ICT (MHRD). Completed the Two – Week ISTE STTP with **GRADE: VERY GOOD.**

38. One Week Faculty Development Programme on "Hands on Experience in Signal Processing Applications" held during 07<sup>th</sup> - 12<sup>th</sup> November 2016 at Gudlavalleru Engineering College Gudlavalleru.

39. Two-day Staff Training Program (STP) on "Simulation of Linear & Digital Integrated Circuits and Microcontroller based Applications" using NI Multisim organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru on 4<sup>th</sup> -6<sup>th</sup> November 2016.

40. Certification for participated in the One Week Faculty Development Programme (FDP) on "Development of Wireless Sensor Network using Network Simulator (NS2)-Hands on Experience", organized by Communication Research Group in Association with Vertex Soft, Hyderabad during 1<sup>st</sup> to 5<sup>th</sup> December 2015 at Gudlavalleru Engineering College, Gudlavalleru.

41. Two-day training program on "Digital Image Processing using MATLAB" organized by Computer Centre, MIT Campus, Anna University, Chennai during 20<sup>th</sup>-21<sup>st</sup> June 2015.

42. One day Seminar on "Research in Signal and Image Processing on FPGAs and Embedded system Platforms" organized by the Institute of Engineers (IE), INDIA, Vijayawada local Center, Vijayawada on 22<sup>nd</sup> November 2014.

43. Attended the Complimentary Training/Orientation on "MATLAB, Simulink & Related Toolboxes for Engineering Education" organized by Akademy of Design & Architecture from 3rd to 4th April 2013 in Gudlavalleru Engineering College, Gudlavalleru.
44. Attended the Five-Day Training Program on "Mentor Graphics (Front end & Back end)and Xilinx Tools" organized by Department of Electronics and Communication Engineering , Gudlavalleru Engineering College, Gudlavalleru in association with IETE Vijayawada Center & IETE Students' Form (ISF-GEC) Coreel Technologies, Bangalore from 13th to17th of September 2010.
45. Certification for participated in the AICTE Sponsored Staff Development Program on "Current Trends in Biometrics", organized by Department of Information Technology during 2nd to 14th November 2009.

## Refresher Courses Attended: 5

1. Attended the One–week Refresher course Course on "Transmission lines and Waveguides", organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, from 29-01-2024 to 02-02-2024.
2. Attended the One–week Refresher course Course on "Microcontrollers and Embedded Systems", organized by Department of Electronics and Communication Engineering, Seshadri Rao Gudlavalleru Engineering College, Gudlavalleru, from 09-01-2024 to 13-01-2024.
3. Attended the One –Week Refresher Course on "Embedded System Design", organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru, from 27th July-1st August, 2020.
4. Attended the One –Week Refresher Course on "Microprocessors and Microcontrollers", during 17-21 November, 2015, organized by Research and Development Center, Department of ECE, Gudlavalleru Engineering College, Gudlavalleru.
5. Attended the One –Week Refresher Course on "Electronic Devices and Circuits", during 23-28 September 2013, organized by Department of Electronics and Communication Engineering, Gudlavalleru Engineering College, Gudlavalleru.

## Faculty Skill Tests.: 02

1. Faculty Skill Test on VLSI Design on 28/05/2020 organized by Department of Electronics and Communication Engineering, Dhanekula Institute of Engineering and Technology, Ganguru, Vijayawada-521139.
2. National Level Quiz on NAAC on 27-5-2020, organized by Department of Computer Engineering, P.V.P.P. College of Engineering, Mumbai-22.

## NPTEL Certifications: 06

1. Successfully completed the 12 Week NPTEL Online certification "Principles of Signals and Systems" course funded by the Ministry of HRD, Govt. of India during January to April 2020 with a consolidated score of **90%**. I received **Gold NPTEL Certification** in this course.
2. Successfully completed the 12 Week NPTEL Online certification "Switching Circuits and Logic Design" course funded by the Ministry of HRD, Govt. of India during July to October 2019 with a consolidated score of 83%.
3. Successfully completed the 12 Week NPTEL Online certification "Principles of Signals and Systems" course funded by the Ministry of HRD, Govt. of India during January to April 2019 with a consolidated score of **77%**.
4. Successfully completed the 12 Week NPTEL Online certification "Digital Circuits" course funded by the Ministry of HRD, Govt. of India during July to October 2018 with a consolidated score of 73%.
5. Successfully completed the 12 Week NPTEL Online certification "Switching Circuits and Logic Design" course funded by the Ministry of HRD, Govt. of India during July to October 2018 with a consolidated score of 77%.
6. Successfully completed the 8 Week NPTEL Online certification "Analog Circuits" course funded by the Ministry of HRD, Govt. of India during January to April 2018 with a consolidated score of 86%.

## Guest Lectures Delivered No.: 01

1. Delivered a lecture on 'FINGERPRINT RECOGNITION SYSTEM'at Gudlavalleru Engineering College, Gudlavalleru in the academic year 2013-2014 for ECE Faculty.

## Subjects Handled:

**Under Graduation**
- Electronics Devices and Circuits
- Basic Electronics
- Elements of Electronics Engineering
- Semiconductor Devices and Circuits
- Linear and Digital ICs
- Electronic System Design
- Pulse and Integrated Circuits
- Pulse and Digital Circuits
- Control Systems
- Analog Communication
- Digital Communication

- Linear IC Applications
- Digital Circuit Design
- Digital Circuits
- Signals and Systems
- Digital Signal Processing
- Coding Theory
- Radar Engineering
- Introduction to Biomedical Engineering

**Post Graduation** -- **NIL**

# Labs Handled:

**Under Graduation**
- Pulse Digital Circuits Lab
- Analog Circuits Lab
- Digital Communications Lab
- LICA Applications Lab
- IC and PDC Lab
- Electronic Devices and Circuits Lab
- Digital Signal Processing Lab
- Programming with Python Lab
- Digital Circuit Design Lab
- Digital Circuit Design Lab through Verilog
- Digital Circuit Design Lab through VHDL
- Microprocessors and Microcontrollers Lab

**Post Graduation** -- **NIL**

# Other Responsibilities:

**College Level (in GEC)–**
- College Level Coordinator for INNOVATION and START-UP Club during 2017-2018 and 2018-2019 academic years
- College Level Convener for MHRD's Innovation Cell.

**Department Level (in GEC)**
- Training and Placement Coordinator from the 2014-2015 academic year to till date.
- Internal Examination Coordinator for the academic years 2010-2011 and 2011-2012

## Projects Guided:

### Under Graduation

| S.No. | Name of the project | Year |
|:-:|:--|:-:|
| 1 | RF BASED AUTOMATIC SPPED LIMITER FOR VEHICLES | 2010 |
| 2 | RFID-BASED INTELLIGENT BOOKS SHELVING SYSTEM | 2011 |
| 3 | INTELLIGENT LIBRARYMANAGEMENT SYSTEM | 2014 |
| 4 | AUTOMATIC ELECRICITY BILLING SYSTEM THROUGH GPS | 2015 |
| 5 | REAL-TIME MULTIFRAME IMAGE SUPER-RESOLUTION | 2016 |
| 6 | BUS TRACKING AND MONITORING SYSTEM USING IOT | 2017 |
| 7 | SMART IRRIGATION MONITORING AND CONTROLLING SYSTEM USING IOT | 2018 |
| 8 | MULTI FACE DETECTION AND RECOGNITION | 2019 |
| 9 | A REAL-TIME BUS TRACKING SYSTEM USING GPS ON SMART PHONE | 2019 |
| 10 | RECONSTRUCTION OF VOICE SIGNALS USING COMPRESSED SENSING | 2020 |
| 11 | RECONSTRUCTION OF SPEECH SIGNALS USING COMPRESSIVE SENSING BASED BASIS PURSUIT ALGORITHM | 2020 |
| 12 | COMPRESSED SENSING IMAGE RECONSRUCTION VIA RECURSIVE SPATIALLY ADAPTIVE FILTERING. | 2021 |
| 13 | COMPRESSED SENSING MRI. | 2021 |
| 14 | PARTICLE SWARM OPTIMIZATION BASED MR IMAGE RECONSTRUCTION | 2022 |
| 15 | LUNG TUMOR CLASSIFICATION AND DETECTION USING DEEP CNN. | 2023 |
| 16 | BRAIN TUMOR DETECTION USING DEEP CNN | 2023 |
| 17 | CLASSIFICATION OF BONE TUMOR USING EFFICIENT-NET B0 | 2024 |

| Sl. No | Name of the project | Year |
|--------|---------------------|------|
| 18 | SKIN CANCER DETECTION USING EFFICIENTNET ARCHITECTURE | 2024 |

**Post-Graduation**

| Sl. No | Name of the project | Year |
|--------|---------------------|------|
| 1 | HARDWARE IMPLEMENTATION OF MODIFIED WEIGHTED MEDIAN FILTERING ON FPGA | 2015 |
| 2 | RECONFIGURABLE LOW POWER FPGA DESIGN WITH LOW DENSITY | 2016 |
| 3 | SATELLITE REMOTE SENSING IMAGE BASED AIRCRAFT RECOGNITION USING WAVELET AND CURVELET TRANSFORMS | 2016 |
| 4 | LBP BASED SHARPNESS PARAMETER BASED APPROACH FOR DEFOCUS BLUR ACTS ON A ROBUST SEGMENTATION ALGORITHM | 2017 |

# Guiding Ph.D Students -- NIL

# Books Published No.:04

1. Co-author for chapter 1 titled "Electronic Devices and Circuits" of Electronics and Communication Engineering laboratory Manual series 2 with ISBN – 978-1-4276-5564-6 in the year 2016
2. Co-author for chapter 3.3 titled "Pulse and Digital Circuit Laboratory" of Electronics and Communication Engineering laboratory manual series with ISBN – 978-1-4276-5563-9 in the year 2016
3. Co-author for chapter 6 titled "Analog Communication" of Electronics and Communication Engineering Instructional manual with ISBN – 978-1-4276-5539-4 in the year 2015.
4. Co-author for chapter 2.1 titled "Electronic Circuit and Pulse and Digital Circuits Laboratory" of Electronics and Communication Engineering laboratory Manual with ISBN – 978-1-4276-5536-3 in the year 2015.

## R&D and Consultancy

| S.No. | Project Title | Source of Funding | Duration | Role | Status |
|-------|---------------|-------------------|----------|------|--------|
| 1 | Wireless Fingerprint Attendance Management System | Internal R&D | 2 Years | Co-Investigator | Completed |
| 2 | Automatic Electricity Billing System using GSM | Internal R&D | 2 Years | Investigator | Completed |
| 3 | Authorized and Enhanced Multifunctional Bus Tracking System | Consultancy | 2 Years | Investigator | Completed |

## Curriculum Design and Development

- Signals and Systems Syllabus Preparation (II Year I Semester ECE -R 23 Autonomous)
- Signals and Systems Lab Syllabus Preparation (II Year I Semester ECE -R 23 Autonomous)
- Signals and Systems Lab Manual Preparation (II Year I Semester ECE -R 23 Autonomous)
- Analog Circuits syllabus Preparation (EEE-Autonomous)
- Pulse Circuits Syllabus Preparation (Autonomous)
- Linear & Digital ICs Syllabus Preparation (Autonomous)
- IC Applications LAB syllabus Preparation (Autonomous)
- Pulse and Digital Circuits LAB syllabus preparation (Autonomous)
- ICApplications Course Module.
- IC Applications Lab Manual.

## Academic Rewards / Achievements

- Got 21st Rank in the Ph. D National -Wide Entrance test conducted by the Pondicherry Central University, Puducherry.
- Got Academic Topper award in M-Tech course.
- Secured State First Rank in PGECET-2011 Entrance Examination.
- Got TWO GOLD MEDALS for COLLEGE FIRST and BRANCH FIRST in DIPLOMA.
- Got 100% Attendance Cash Award during the B-Tech II year.

## Extra-Curricular Activities

- Acted as Class Teacher for III Year –B Section during 2019-2020 academic year I Semester.

- Acted as the PRESS and MEDIA Coordinator for the First International Conference on "CIATES-2K18" during 30th November to 1st December 2108 held at Gudlavalleru Engineering College.

- Acted as coordinator for various JOB Awareness Programs- Guest Lectures given Various Company HRs and CEOs

- Acted as the coordinator for Various Placement Recruitment Drives.

- Acted as the Coordinator for Various INNOVATION and Start -Up Club Events

- Acted as the Project coordinator for IV Year Section –A during II semester 2017-2018 Academic year.

- Acted as Class Teacher for III Year –D Section during 2018-2019 academic year I Semester.

- Acted as Class Teacher for III Year –C Section during 2018-2019 academic year II Semester.

- Enacted as BoS member for the Autonomous curriculum design of Electronics and Communication Engineering at Gudlavalleru Engineering College, Gudlavalleru on 2nd August 2014 and 25th May 2015.

- Prepared the 2 Experiment Modules in the "Electronic Devices Laboratory Manual" for R17 regulation Syllabus.

- Prepared the 3 Experiment Modules in the "Digital Circuit Design" for R17 regulation Syllabus.

## Declaration

I hereby declare that all the above-furnished information is correct to the best of my knowledge.

Date:  1-2-2025                                              **Signature**

Place:  Gudlavalleru                                          (P.Anil Kumar)